

## AMENDMENTS TO SPECIFICATION

Please replace the first full paragraph on page 6 with the following amended paragraph:

However, the partial input voltage to be detected may be much higher than the reference detection voltage and thus the voltage detection cannot be effectively performed. For this, cascoded transistor pairs 51, 52 are provided as shown in FIG. 5. The stage of cascoded transistor pairs depends on the voltage to be detected. That is, when the voltage to be detected is close to the reference detection voltage (the voltage of N1 node, which is  $V_{BG}$ ), the number of cascoded transistor pairs is 1, which applies only one transistor pair 51. When the voltage to be detected is close to twice as high as the reference detection voltage (voltage of N1 node), the stage of cascoded transistor pairs is 2, which applies two transistor pairs 51, 52. Accordingly, the detection voltage can ~~keep in~~ be kept at a steady level.